Searching PAJ Page 1 of 2

PATENT ABSTRACTS OF JAPAN

(11)Publication number: 03-187169

(43) Date of publication of application: 15.08.1991

(51)Int.Cl. H01R 13/64 H01R 23/68

(21)Application number: 01-325229 (71)Applicant: FUJITSU GENERAL LTD

(22)Date of filing: 15.12.1989 (72)Inventor: SUZUKI YASUSHI

TAKAHACHI NOBUAKI

KAKIMOTO KOJI SAITO MASAAKI IWANO TORU

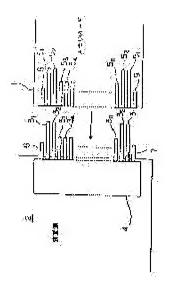
(54) IC CARD CONNECTING DEVICE

(57)Abstract:

.....

PURPOSE: To prevent malfunction or breakage of a memory element in pulling and inserting a memory card by stopping supply from a power source to the memory card at the time of connecting or disconnecting terminals of a power source line with or from each other and connecting or disconnecting a signal line.

CONSTITUTION: The main body of a device 2 is provided with the shortest lead detecting terminals 6, 7 of all the terminals, and a control circuit for controlling inner lines such as a power source system and a signal system based on signals from the detection terminals 6, 7, and a memory card 1 is provided with terminals 51~ 5n, and terminals 8, 9 to be connected with the detecting terminals 6, 7, as well. In pulling and inserting the



memory card, supply of power to the memory card is stopped at the time of connecting or disconnecting the shortest lead detecting terminal on the device main body side with or from the memory card terminal. Thus, as supply of the power to the memory card is stopped at the time of connecting or disconnecting the power source line or the signal line, malfunction of a memory element or breakage of data can be prevented in pulling or inserting the memory

Searching PAJ Page 2 of 2

card.